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39. A transceiver according to claim 38, wherein said DC voltage regulator produces at least two DC voltage outputs, and further includes:

a switch for inhibiting a first of said two DC output voltages when a second of said two DC voltage outputs is above a predetermined threshold.

- 40. A transceiver according to claim 29, further including: both said modulator and said demodulator.
- 41. A transceiver according to claim 40, further including:
 a single local oscillator for modulating an intermediate frequency of said
 modulator, and for demodulating an intermediate frequency of said demodulator.
 - 42. A transceiver comprising:

at least one of a modulator for modulating information and a demodulator for demodulating information; and

a housing within which said at least one of modulator and demodulator is mounted, components used for at least one of modulation and demodulation of said information being mounted directly to said housing.

- 43. A transceiver according to claim 42, wherein said housing is made of a first material, and electrical components of said at least one modulator and demodulator mounted to said housing are made of a second material, said first material having a coefficient of thermal expansion material matched to that of said second material.
- 44. A transceiver according to claim 43, wherein said housing is hermetically sealed.
 - 45. A transceiver according to claim 43, wherein said first material is silvar.

- 46. A transceiver according to claim 43, wherein said first material is a ceramic.
- 47. A transceiver according to claim 43, wherein said first material is a metal.
 - 48. A transceiver for wireless communication of information, comprising: a modulator for transmitting modulated information with a power output of 0.5 W or greater; and

an antenna for transmitting said information wirelessly.

- 49. A transceiver according to claim 48, said modulator further including: a local oscillator for modulating an intermediate frequency of 2-3 GHz with a frequency on the order of 18 GHz.
- 50. A transceiver according to claim 48, wherein said modulator further includes:

plural, parallel amplification channels.

- 20 51. A transceiver according to claim 48, wherein said antenna is a dual polarization antenna which includes:
 - a transmission antenna; and
 - a reception antenna separated by a distance from said transmission antenna.
- 25 52. A transceiver according to claim 48, wherein said antenna is a dual polarization antenna which includes:
 - a single antenna having a dual polarization capability for transmitting information with a first polarization, and for receiving information with a second polarization.

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- 53. A transceiver according to claim 48, further including: at least one DC voltage regulator for providing a regulated DC output voltage.
- 54. A transceiver according to claim 53, wherein said DC voltage regulator produces at least two DC output voltages, and further includes:

a switch for inhibiting a first of said two DC output voltages when a second of said two DC voltage outputs is above a predetermined threshold.

- 55. A transceiver according to claim 48, further including:
 a demodulator for demodulating information signals; and
 a single local oscillator for modulating an intermediate frequency of said
 modulator, and for demodulating an intermediate frequency of said demodulator.
- 56. Apparatus for wireless communication of information comprising: at least one of a signal modulator for producing information signals and a signal demodulator for receiving said information signals, configured using at least one monolithic millimeter wave integrated circuit; and

an antenna for at least one of wireless transmission and wireless reception of said information signals.

- 57. Apparatus according to claim 56, wherein said modulator modulates data received on an intermediate frequency of 2-3 GHz.
- 58. Apparatus according to claim 57, further including:
 25 a local oscillator for modulating said data with a frequency on the order of 18

 GHz.
 - 59. Apparatus according to claim 57, wherein said apparatus includes: a transmitter having plural, parallel amplification channels.